? ds

Set	Items	Description
S1	9706	(CONVERT? OR CONVERS? OR CHANG? OR MODIF? OR ADJUST? OR AL-TER?)(5N)BUS
S2	349	S1(10N)SERIAL?(5N)PARALLEL?
S3	401	S1(5N)(PARTITION? OR DIVID? OR SEPERAT? OR DIVISION? OR PART OR PARTS OR SECTION?? OR SEGMENT?? OR PORTION?? OR FRAGMENT? OR PIECES OR SECTOR??)
S4	1068	ARRAY?(7N)(MEMORY OR BUFFER?? OR STORAGE OR STORAGE(3N)CELLS)(7N)PORT??
S5	58	(ONE OR SINGLE OR 1)()BUFFER?(3N)ELEMENT??
S6	75	CLOCK(5N)(CONTROL OR MONITOR OR DIRECT?)(5N)ACCESS???(7N)(SEQUENTIAL? OR SIMULTANEOUS? OR CONCURRENT? OR COINCIDENT?)
S7	6644	(WRITE OR READ)(3N)CYCLE??
S8	26	AU=(ALOWERSSON, J? OR ROSLUND, B? OR SUNDSTROM, P? OR ALOWERSSON J? OR ROSLUND B? OR SUNDSTROM P?)
S9	15764	(PRESELECT? OR (PRE()(SELECT? OR SET OR DETERMIN? OR SELECT? OR SPECIFIED) OR PREDETERMIN? OR SPECIFIC OR SPECIFIED OR SET OR PRESET))(3N)CYCLE?
S10	35872	IC=H04Q?
S11	2	S3(S)S4
S12	3	S3(S)S5
S13	2	S12 NOT S11
S14	2	S6(10N)S7
S15	1	S14 NOT (S12 OR S11)
S16	0	S1 AND S8
S17	0	S6 AND S8
S18	3	S8 AND BUFFER?
S19	3	S18 NOT (S14 OR S12 OR S11)
S20	11	S9(S)S3
S21	1	S20 AND S10
S22	1	S21 NOT (S18 OR S14 OR S12 OR S11)
S23	1	S3(S)S6
S24	0	S23 NOT (S21 OR S18 OR S14 OR S12 OR S11)

00306062
Digital data processing system.
Digitales Datenverarbeitungssystem.
Systeme du traitement de donnees numeriques.
PATENT ASSIGNEE:
  DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
      , (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)
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LEGAL REPRESENTATIVE:
  Robson, Aidan John et al (69471), Reddie & Grose 16 Theobalds Road,
      London WC1X 8PL, (GB)
PATENT (CC, No, Kind, Date):   EP 300516   A2   890125 (Basic)
                               EP 300516   A3   890426
                               EP 300516   B1   931124
APPLICATION (CC, No, Date):   EP 88200921 820521;
PRIORITY (CC, No, Date): US 266413 810522; US 266539 810522; US 266521
      810522; US 266415 810522; US 266409 810522; US 266424 810522; US 266421
      810522; US 266404 810522; US 266414 810522; US 266532 810522; US 266403
      810522; US 266408 810522; US 266401 810522; US 266524 810522
DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE
RELATED PARENT NUMBER(S) - PN (AN):
  EP 67556 (EP 823025960)
INTERNATIONAL PATENT CLASS: G06F-009/46; G06F-012/14;
ABSTRACT WORD COUNT: 122

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1018
CLAIMS B	(German)	EPBBF1	868
CLAIMS B	(French)	EPBBF1	1115
SPEC B	(English)	EPBBF1	154256
Total word count - document A			0
Total word count - document B			157257
Total word count - documents A + B			157257

...SPECIFICATION in information transfer, are effectively skipped over so
  that access to MEM 112 is dynamically **modified** according to the
  information transfer loads of the various data channel devices. RGG
  1644's...written into MEM 10112 from backing store (ED 10124). For
  example, if a page's **modified** bit indicates that that page has not been
  written into, it is not necessary to...10112 may therefore have a
  physical capacity of up to, for example, 16 Mega-bytes **of** bulk **storage**
  . As will be described further below. MA 20112's of different capacity

may be used **together** in MSB 20110, for example, four 2 Mega-byte MA
20112's and four 1 Mega-byte **MA 20112** 's.
BC 20114 controls operation of MA's 20112 and is the path for transfer
...for example, may be limited from making full use of MEM 10112's
address space. **Each** port has a different set of allowed operations. For
example, JO **Port** can use a bit granular addresses but can reference
only **32 bits** of data on each **request** . JI **Port** can make read
requests only to word align 32 bit data items. IO Port may...
...data, and, as described further below, may read or write up to 16 bytes
on **each** read or write request. The characteristics of each of these
ports will be discussed next below.
1. IO **Port** Characteristics
IOS 10116 may access MEM 10112 in either of two modes. The first mode
...

 11/3,K/2 (Item 1 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00234265 **Image available**
**SYSTEM FOR DIVIDING PROCESSING TASKS INTO SIGNAL PROCESSOR AND
DECISION-MAKING MICROPROCESSOR INTERFACING**
**SYSTEME DE SEPARATION DES TACHES DE TRAITEMENT EN TACHES POUR INTERFACAGE
AVEC UN PROCESSEUR DE SIGNAUX ET UN MICROPROCESSEUR DE PRISE DE
DECISION**
Patent Applicant/Assignee:
 STAR SEMICONDUCTOR CORPORATION,
Inventor(s):
 ROBINSON Jeffrey I,
 ROUSE Keith,
 KRASSOWSKI Andrew J,
 MONTLICK Terry F,
Patent and Priority Information (Country, Number, Date):
 Patent: WO 9308524 A1 19930429
 Application: WO 92US8954 19921014   (PCT/WO US9208954)
 Priority Application: US 91776161 19911015
Designated States: AU CA JP KR AT BE CH DE DK ES FR GB GR IE IT LU MC NL SE
Publication Language: English
Fulltext Word Count: 219172
Fulltext Availability:
 Claims

Claim
... invention;
 Figures 5a and 5b are block diagrams of the serial input and serial
 output **ports** of the
 invention;
 Figure 6 is a simplified block diagram of the host **port** of the
 invention;
 Figure 7 is a block diagram of the access port of the...master SPROC 10
 read the EPROM 170 and forward the information via the common host **bus**
 165 to the other SPROCs which reside in different address spaces. The
 slave SPROCs do...mode position. In order to program the SPROC, the host
 180 preferably utilizes the host **bus** 165 and sends program data via
 host **port** 800, and program RAM bus 155 to the program RAM, and data RAM
 data via...

...serial data flow into and out of the SPROC 10 is primarily through the
 serial **ports** 700, while parallel data flows through the host **port**

800. Serial data which is to be processed is sent into an input **port** 700 which is coupled to the data flow manager 600, which in turn forwards the input or output serial **ports** 700. Similarly, the GSPs do not have direct access to the host **port** 800 or the access port 900. Thus, in order for the processed data to be...bus flags as is discussed hereinafter, while addresses 0814 through 0fff are used as data **buffers**, scratch pad locations, etc. Of the auxiliary space, certain addresses are used as follows:

0401H Exit boot mode (write f0H) (generate GSP hard reset)
0405H Serial **port** reset (write)
H Global break entry (write.) (generate GSP soft -reset)
0407H Global break exit...

...to 048fH DAC (probe) serial output port
0.4fcH to 04ff R Host interface registers
Memory locations 1000 to ffff Hex refers to external address space (e.g. slave SPROCs, other devices, or **memory**). Of the auxiliary **memory** locations in the data RAM 100, it should be noted that each GSP is given
...

...allow for individual GSP breaks. The eight twenty-four bit locations provided for each serial **port** are used to configure the serial ports as well as the DFM section associated with each serial **port** as hereinafter described. Similarly, the eight words of memory assigned the input and output **ports** of the probe are used to configure the probe, while the eight words of memory assigned the host **port** are used to configure the host **port** as described hereinafter. Further, with regard to the memory locations, it is noted that when...the data RAM bus as a twenty-four bit word is conducted by the serial **port** 700, as hereinafter described. Besides the data flow circuitry, each DFM is arranged with **buffers** , counters, gates, etc. to generate data RAM FIFO addresses for the incoming data. As shown...data RAM bus. That data is received by the DFM and latched and stored in **buffer** 694 prior to being forwarded to the serial output **port** 700b. The remaining circuitry of Fig. 4b serves the functions of not permitting the data to be forwarded to the serial output **port** 700b unless certain conditions (i.e. triggers) are met, as well as generating synch pulses...the value of the offset counter 656 is equal to the FIF0 length stored in **buffer** 680, a sync pulse is generated by bus wide XNOR gate 686 which is used...a twenty-four bit word to the data RAM bus 125 or the program RAM **bus** 155 of the SPROC. Where the host processor is an eight bit processor as indicated...9, the access port 900 is comprised of a shift register 9 1 0, a **buffer** 920, a decoder 925, and a switch 930 on its input side, and a multiplexer...

...parallel load shift register 950 on its output side. On its input side, the access **port** 900 receives serial data as well as a clock and strobe signal from the development...capabilities. Typically, the SPROC devices are coupled and communicate with each other via their serial **ports** 700, although it is possible for the SPROCs to communicate via their parallel host **ports** 800. The system of SPROCs can act as a powerful signal processing front end to...The commands read and probe allow the user to view the value of a given **memory** location, either by accessing it directly by address or symbolically by symbol name. The read...bit operand as an offset to the base
register (register B) to determine the data
memory address.
base, loop [B+L+xxx] Use the 15-bitoperand as an offset to the base indexed register (register B) plus the loop register
(register L) to determine the data **memory**
address.. fi-ame indexed [F + xxx] Use the 15-bit operand as an offset to

the fi-ame
pointer register (register F) to determine the data
 memory address. frame loop [F + L + xxx] Use the 15-bit operand as an
offset ...indexed pointer register (register F) plus the loop register
(register L) to determine the data **memory**
address. If offset is zero, +0 is optional. For imp and conditional jxx
instructions, the...
...bit operand as an offset to the base register (register
B) to determine the data **memory** address containing the jump
destination. indirect base loop Use the 1 5-bit operand as...phantom
blocks automatically added, which for each signal, writes successive
values to an additional single **memory** location so that it may be probed
at a single **memory** address. The scheduler/compiler supports
asynchronous timing as well as decimation and interpolation. Decimation
and interpolation are accomplished within temporal partitioning by
"blocking" the signal values into **arrays** , and operating on these
arrays of values rather than on single signal values. Thus, for example,
in decimating by four, four input samples are **buffered** up by the input
data flow manager. The code blocks before the decimator are looped...
identifies the complete data structure. This data structure occupies 4K
by 4 bytes of microprocessor **memory** . The variable declared to identify
the structure is the sproq
id, and the
variable name...
?

13/3,K/1   (Item 1 from file: 348)
DIALOG(R)File 348:EUROPEAN PATENTS
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00306058
Digital data processing system.
Digitales Datenverarbeitungssystem.
Systeme de traitement de donnees numeriques.
PATENT ASSIGNEE:
  DATA GENERAL CORPORATION, (410940), Route 9, Westboro Massachusetts 01581
    , (US), (applicant designated states: AT;BE;CH;DE;FR;GB;IT;LI;LU;NL;SE)
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LEGAL REPRESENTATIVE:
  Robson, Aidan John et al (69471), Reddie & Grose 16 Theobalds Road,
    London WC1X 8PL, (GB)
PATENT (CC, No, Kind, Date):   EP 290111   A2   881109 (Basic)
                               EP 290111   A3   890503
                               EP 290111   B1   931222
APPLICATION (CC, No, Date):   EP 88200917 820521;
PRIORITY (CC, No, Date): US 266404 810522
DESIGNATED STATES: AT; BE; CH; DE; FR; GB; IT; LI; LU; NL; SE
RELATED PARENT NUMBER(S) - PN (AN):
  EP 67556 (EP 823025960)
INTERNATIONAL PATENT CLASS: G06F-009/30;
ABSTRACT WORD COUNT: 123

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1044
CLAIMS B	(German)	EPBBF1	890
CLAIMS B	(French)	EPBBF1	1185
SPEC B	(English)	EPBBF1	154314
Total word count - document A			0
Total word count - document B			157433

Total word count - documents A + B 157433

...SPECIFICATION SEBs 46864 (Figs. 270, 468, 469, 470, 471, 472)
          d.d. Cross-Procedure Object Calls (**Figs** . 270, 468, 469, 470,
   471, 472)
          e.e. Cross-Domain Calls (Figs. 270, 408, 418...of MEM 10112
   address space.
     In operation, FU 10120 reads data and instructions from MEM **10112** by
   providing physical addresses to MEM 10112 **by** way of PA Bus 10146 and
   MJP Port 10140. The instructions and data are transferred...

 13/3,K/2   **(Item 1 from file: 349)**
DIALOG(R)File 349:PCT FULLTEXT
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00781825
SYSTEM OF REUSABLE SOFTWARE PARTS AND METHODS OF USE
SYSTEME D'UNITES LOGICIELLES REUTILISABLES ET PROCEDES D'UTILISATION
Patent Applicant/Assignee:
  Z-FORCE CORPORATION, 151 Kalmus Drive, Suite B-250, Costa Mesa, CA 92626,
    US, US (Residence), US (Nationality)
Inventor(s):
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  NICKOLOV Peter A, 158 Giotto, Irvine, CA 92614, US,
Legal Representative:
  TACHNER Adam H (et al) (agent), Crosby, Heafey, Roach & May, Suite 2000,
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Patent and Priority Information (Country, Number, Date):
  Patent: WO 200114959 A2-A3 20010301 (WO 0114959)
  Application: WO 2000US22694 20000816 (PCT/WO US0022694)
  Priority Application: US 99149371 19990816; US 99149624 19990816
Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ
  DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ
  LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG
  SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
  (EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE
  (OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
  (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
  (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 182432

Fulltext Availability:
  Detailed Description

Detailed Description
... flow, so that an existing flow of events can be transformed into a
   desirable one.

   1 5.One other advantage of the present invention is that it provides
   reusable parts that...

...other parts.

   1 7. Another advantage of the present invention is that it provides
   reusable **parts** that **convert** incoming calls or synchronous requests
   into pairs of asynchronous interactions consisting of requests and
   replies...the first connection between a third object
   contained in the container and the second object.

DIALOG(R)File 348:EUROPEAN PATENTS

00307708
Multi-access device.
Mehrfachzugriffsvorrichtung.
Dispositif d'acces multiple.
PATENT ASSIGNEE:
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    XX Amsterdam, (NL), (applicant designated states:
    CH;DE;ES;FR;GB;IT;LI;NL;SE;AT)
  BELL TELEPHONE MANUFACTURING COMPANY Naamloze Vennootschap, (268511),
    Francis Wellesplein 1, B-2018 Antwerp, (BE), (applicant designated
    states: BE)
INVENTOR:
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    (BE)
  Haspeslagh, Didier Rene, Tieltsestraat 10, B-8750 Harelbeke, (BE)
LEGAL REPRESENTATIVE:
  Vermeersch, Robert et al (1162), BELL TELEPHONE MANUFACTURING COMPANY
    Naamloze Vennootschap Patent Department Francis Wellesplein 1, B-2018
    Antwerpen, (BE)
PATENT (CC, No, Kind, Date):   EP 320041   A2   890614 (Basic)
                            EP 320041   A3   900328
                            EP 320041   B1   940330
APPLICATION (CC, No, Date):   EP 88202675 881124;
PRIORITY (CC, No, Date): BE 8701402 871207
DESIGNATED STATES: AT; BE; CH; DE; ES; FR; GB; IT; LI; NL; SE
INTERNATIONAL PATENT CLASS: G06F-013/18;
ABSTRACT WORD COUNT: 150

LANGUAGE (Publication,Procedural,Application): English; English; English
FULLTEXT AVAILABILITY:

Available Text	Language	Update	Word Count
CLAIMS B	(English)	EPBBF1	1008
CLAIMS B	(German)	EPBBF1	841
CLAIMS B	(French)	EPBBF1	1130
SPEC B	(English)	EPBBF1	6986
Total word count - document A			0
Total word count - document B			9965
Total word count - documents A + B			9965

...SPECIFICATION DS, ALS, UPA and RMB are then de-activated and the
processor PR acknowledges this **read** operation by de- **activating** the
signal RD whose rear edge r2 causes the **control** signal DA to be **de**
-activated in turn by CLC.
    Three examples of **simultaneous**   **access** to the device MAM **are**
shown in the Fig. 3, parts b, c and d respectively. The signals **shown**
in these examples make reference to the above mentioned signals C4, SOS
and SIS and...
?

00939958
METHODS FOR REGULATING BUD-HYPHA TRANSITIONS AND cAMP LEVELS BY THE ADENYLATE CYCLASE-ASSOCIATED PROTEIN GENE, CAP1
METHODES DE REGULATION DE TRANSITIONS BUD-HYPHA ET DE NIVEAUX DE CAMP PAR LE GENE PROTEIQUE CAP1 ASSOCIE A L'ADENYLATE CYCLASE
Patent Applicant/Inventor:
   SUNDSTROM Paula , One Miranova Place, Suite 1025, Columbus, OH 43215, US
   , US (Residence), US (Nationality
Legal Representative:
   PELTO Don (et al) (agent), McKenna & Cuneo LLP, 1900 K Street NW,
   Washington, DC 20006, US,
Patent and Priority Information (Country, Number, Date):
   Patent: WO 200272129 A1 20020919 (WO 0272129)
   Application: WO 2002US6986 20020308   (PCT/WO US0206986)
   Priority Application: US 2001801774 20010309
Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CO CR CU
   CZ DE DK DM DZ EC EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP
   KR KZ LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ OM PH PL PT RO
   RU SD SE SG SI SK SL TJ TM TN TR TT TZ UA UG UZ VN YU ZA ZM ZW
   (EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR
   (OA) BF BJ CF CG CI CM GA GN GQ GW ML MR NE SN TD TG
   (AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZM ZW
   (EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 27724

Patent Applicant/Inventor:
   SUNDSTROM Paula ...
Fulltext Availability:
   Detailed Description

Detailed Description
...   to an element or compound found in the presence of (if anything) only
   a solvent, **buffer** , ion, or other component normally present in a
   solution of the same.

   Nucleic acid sequences...administration to the recipient.

   Such pharmaceutically acceptable carriers include, for example, isotonic
   saline and other **buffers** as appropriate to the patient and therapy. The
   genetically modified cells are administered by, for...

   ...followed by scraping off a portion of the epithelial layer; (2)
   injection of 0. 1 % **buffered** trypsin for 20 minutes
   40
   followed by scraping; (3) removal of epithelial cells by gentle...vehicle
   containing physiologically compatible substances such as sodium chloride,
   glycine and the like, having a **buffered** pH compatible with physiologic
   conditions. Such intravenous delivery vehicles are known to those skilled
   in...0) plus 10% bovine calf serum (Feng et al., supra), 10 mM
   imidazole-HC I **buffer** media (pH 7.0) containing 0.2 mM $MnCl_2$ with the
   following inducing agents : 1...to strains with CAP1.

   Similar results were found in 10% serum with 50mM potassium phosphate
   buffer (pH 6.0).

The ability of cap1lcap1 mutant cells to form germ tubes upon prolonged
...primer extension under conditions sufficient for first strand cDNA
synthesis, where additional reagents include dNTPs; **buffering** agents,
e.g. Tris.Cl; cationic sources, both monovalent and divalent, e.g. KCI,
MgCI2...

...primer.

The primer/RNA mix is incubated followed by the addition of first strand
reaction **buffer** , DTT, dNTPs, RNasin, and Superscript II (Gibco BRL) to
the mix. Following a second incubation period, second strand synthesis
buffer , dNTPs, DNA polymerase, R.Nase, DNA figase, and RNase-free water
are added. Following a...

...kit may be used to amplify RNA. In a inicrofage tube, doublestranded
cDNA, RNA polymerase **buffer** , ATP, CTP, GTP, UTP, DTT, and RNA
polymerase are·added and then incubated. The amplified...

...chilled on ice, and then equilibrated at room temperature. For the
initial reaction, first stand **buffer** , DTT, dNTPs, RNasin, and reverse
transcriptase are added to the aRNA mix, and then incubated...

...is added to the aRNA reaction mix and the sample is incubated. Second
strand synthesis **buffer** , dNTPs, DNA Polymerase, R.Nase, DNA ligase, and
RNase-free water are added to the...

...RNase-free water, heated, and then chilled on ice. For the labeling
reaction, first strand **buffer** , DTT, R.Nasin, d(GAT)TP, dCTP,
labeled-dCTP, and reverse transcriptase are added to...

...Columns and Qiagen Nucleotide Removal Columns. The probes are
vacuum-dried and resuspend in hybridization **buffer** .

Microarray matrices are treated to ensure arnino-linkage of cDNAs to the
slides, and then...

 19/3,K/2   (Item 2 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00805044   **Image available**
METHODS FOR ALTERING THE EXPRESSION OF HYPHAL-SPECIFIC GENES
PROCEDES DE MODIFICATION DE L'EXPRESSION DES GENES SPECIFIQUES DE L'HYPHE
Patent Applicant/Inventor:
   SUNDSTROM Paula , 247 E. Beck Street, Columbus, OH 43206, US, US
      (Residence), US (Nationality
Legal Representative:
   PELTO Don (et al) (agent), Mckenna & Cuneo, 1900 K Street, NW,
      Washington, DC 20006, US,
Patent and Priority Information (Country, Number, Date):
   Patent: WO 200138550 A2-A3 20010531 (WO 0138550)
   Application: WO 2000US32464 20001129 (PCT/WO US0032464)
   Priority Application: US 99167672 19991129
Designated States: AE AG AL AM AT AU AZ BA BB BG BR BY BZ CA CH CN CR CU CZ
   DE DK DM DZ EE ES FI GB GD GE GH GM HR HU ID IL IN IS JP KE KG KP KR KZ
   LC LK LR LS LT LU LV MA MD MG MK MN MW MX MZ NO NZ PL PT RO RU SD SE SG
   SI SK SL TJ TM TR TT TZ UA UG UZ VN YU ZA ZW
   (EP) AT BE CH CY DE DK ES FI FR GB GR IE IT LU MC NL PT SE TR

(OA) BF BJ CF CG CI CM GA GN GW ML MR NE SN TD TG
(AP) GH GM KE LS MW MZ SD SL SZ TZ UG ZW
(EA) AM AZ BY KG KZ MD RU TJ TM
Publication Language: English
Filing Language: English
Fulltext Word Count: 32629

Patent Applicant/Inventor:
   SUNDSTROM Paula ...
Fulltext Availability:
  Detailed Description

Detailed Description
... to an element or compound found in the presence of (if anything) orily
   a solvent, **buffer** , ion, or other component nori-nally present in a
   solution of the same.

   Nucleic acid...administration to the recipient.

   Such phannaceutically acceptable carriers include, for example, isotonic
   saline and other **buffers** as appropriate to the patient and therapy. The
   genetically modified cells are administered by, for...

...followed by scraping off a portion of the epithelial layer; (2)
   injection of 0. 1 % **buffered** trypsin for 20 minutes followed by
   scraping; (3) removal of epithelial cells by gentle scraping...vehicle
   containing physiologically compatible substances such as sodium
   cliloride, glycine and the like, having a **buffered** pH compatible with
   physiologic conditions. Such intravenous delivery vehicles are known to
   those skilled in...7(8) GENEs DEV. 1598-608 (1993). Cefis are washed,
   lysed with glass beads in **buffer** with protease inhibitors, clarified by
   low speed centrifugation followed by ultracentrifugation to remove
   insoluble material...

...regions and are end labeled with [YJ'IATP.

   Extracts and probes are incubated in binding **buffer** containing
   glycerol, and poly(dl:dC) prior to separation by electrophoresis with a
   high-Ionic...

...UV light, followed by elution from the non-denaturing gel,
   solubilization in LaernmII-SDS sample **buffer** and separation on SDS-PAGE
   gels, followed 66 by autoradiography as previously described by Chodosh
   ...

...extracts from cells grown in appropriate media are incubated with the
   DNA-beads in binding **buffer** containing poly (dl-dC) and washed,
   followed by elution of the DNA-binding protein with...

 19/3,K/3   (Item 3 from file: 349)
DIALOG(R)File 349:PCT FULLTEXT
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00561400
**INHIBITION   OF TRANSGLUTAMINASE-MEDIATED MICROBIAL INFECTION OF A MAMMALIAN
      HOST**
**INHIBITION DE L'INTERACTION MICROBIENNE INDUITE PAR LA TRANSGLUTAMINASE
      AVEC UN MAMMIFERE HOTE**
Patent Applicant/Assignee:

Detailed Description
...   transgiutaminase (8.5 @tg), and 5-(biotinamido)pentylamine (30 @tM)
  (Pierce) were incubated in reaction **buffer** 1 (400 @tl) (I 00 MM Tris-CI
  pH 7.5, 5 MM CaCI2, I...NEN, 1 08-1 1 0 mCi/mmol) and transglutaminase
  (3.4 @Lg) in reaction **buffer** 2 (I 00 mM Tris-Cl pH 7.5, 20 mM CaC12, 1
  mM DTT...

...1 0 mCi/mmol) and 3.4 @Lg of transglutaminase in 25 @d of reaction
  buffer 2, supra.

  Transglutaminase was inhibited with EGTA (20 mM) or iodoacetamide (20
  mM).

  Reactions were...

...with cold putrescine (I 00 mM) for one hour., and boiled in Laemm.li
  sample **buffer** for 5 minutes. A portion of each sample was analyzed by
  SDS-PAGE followed by...
...Washed germ tubes and BECs from a healthy donor, suspended in 300 91 of
  reaction **buffer** 3 (50 mM Tris-Cl pH 7.5, 10 mM CaCI2, 1 mM EDTA, I...

...PBS containing iodoacetamide (10 mM) for 15 minutes at 37'C, and
  suspended in reaction **buffer** supplemented with iodoacetamide (IO mM)
  prior to incubation with germ tubes.

  UnoPP-1, a CA14...

...ratio of I 00: I with or without 5 or IO mM monodansylcadaverine in
  reaction **buffer** 3, supra. The monodansylcadaverine was added last to
  the reaction mix from a 1 00...vehicle containing physiologically
  compatible substances such as sodium chloride, glycine and the like,
  having a **buffered** pH
  ible with physiologic conditions. Such intravenous delivery vehicles are
  compati
  known to those skilled...administration to the recipient. Such
  pharmaceutically acceptable carriers include, for example, isotonic
  saline and other **buffers** as appropriate to the patient and therapy.

The genetically modified cells are administered by, for...
...followed by scraping off a portion of the epithelial layer; (2)
injection of 0. 1 % **buffered** trypsin for 20 minutes followed by
scraping; (3) removal of epithelial cells by gentle scraping...
?

00741338
**Connectionless   communications   system,   test   method,   and   intra-station
    control system**
**Verbindungsloses Kommunikationssystem, Testmethode und Intra-Station-Steuer
    ungssystem**
**Systeme de communication sans connection, methode de test et systeme de
    gestion intra-station**
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PATENT (CC, No, Kind, Date): EP 700229 A2 960306 (Basic)

...SPECIFICATION firmware on a cycle of 10ms. If the same data is read
  consecutively for 2 **cycles** , then the data is fetched in the firmware.

 15.1.1.7. Active Control Function...
?